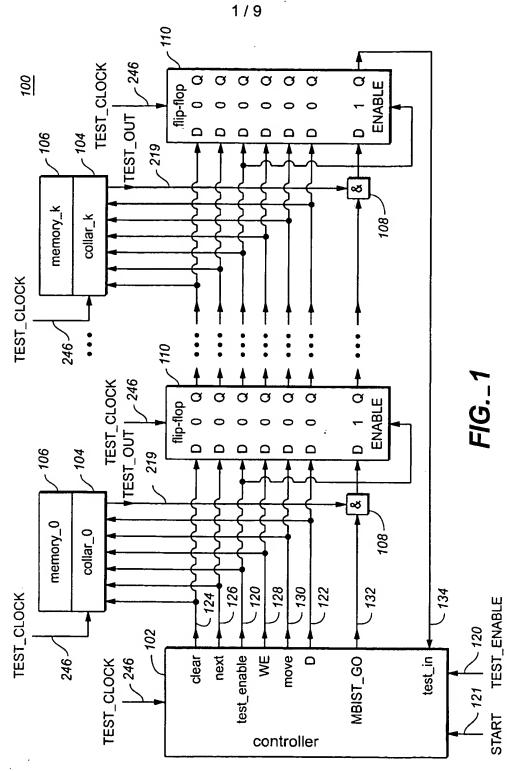
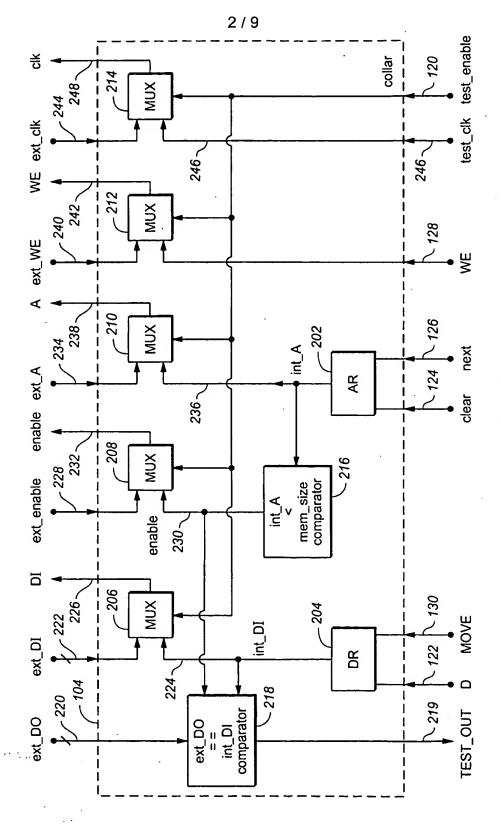
Drawing approved 1/11/05 784 Replace all previously submitted discovering, pot

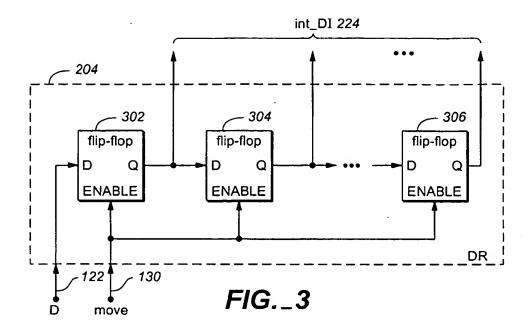


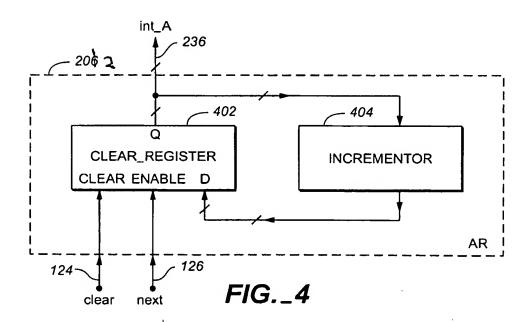




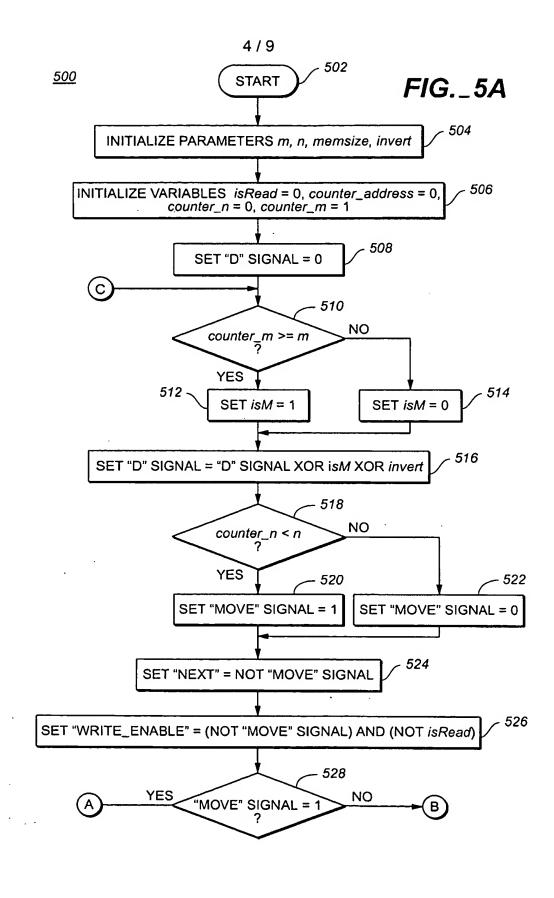
F/G._2



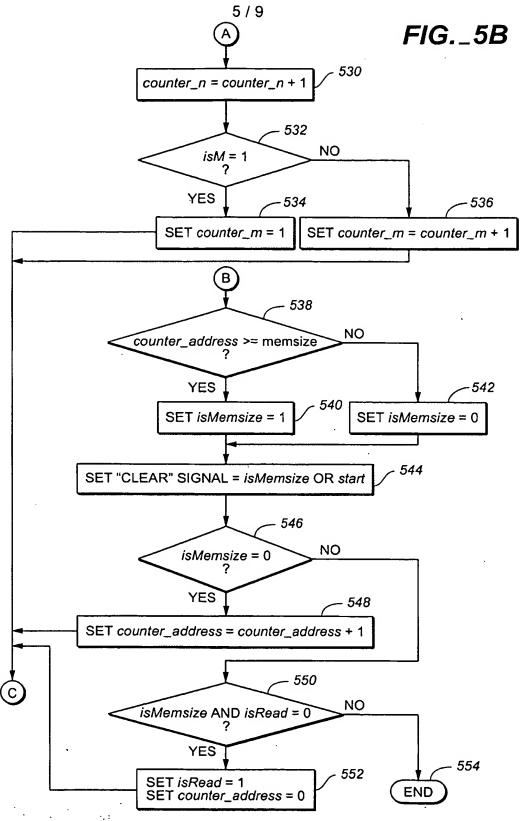












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							6/9							FIG8	
. (. [0	0	—	0	M F/G6	1	808	memory read memsize+[memsize]steps)	0	0 1 00 1	.1 0 11 0	0	$\alpha \overline{\alpha} \overline{\alpha} \cdots \overline{\alpha} \alpha \cdots$	}*
909	memory read (memsize steps)	1 0	00	1 1	00	α⊕ is M ••• α⊕ is M		908	fill DR(n steps) mer	0 0 00	1 0	0 1	0 0	101 α αα	} c
604	memory write (memsize steps)	0 00	0 00		1	α⊕ is M ··· α⊕ is M		804	memory write memsize+[memsize]steps) fi	0	.0 1 00 1 1	.10110 0	.10110 0	α α α…α α … 0	}*
602	fill DR(n steps)	100 0	111 1	0 000	0 000	001100α	} E } E	805	fill DR(n steps) merr	1000 0 0	1111 1 00	0 0 0 0 0 1	0 0 0 0 0 1	0 1 0 1 α α αα	}~
		clear	move	next	WE	٥				clear	move	next	WE	O	



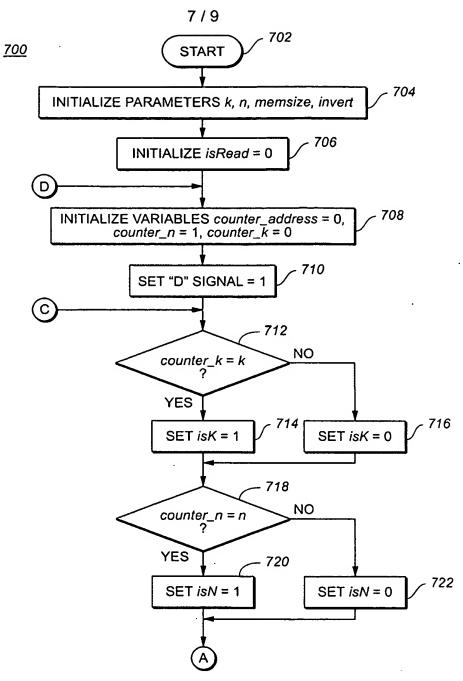


FIG._7A



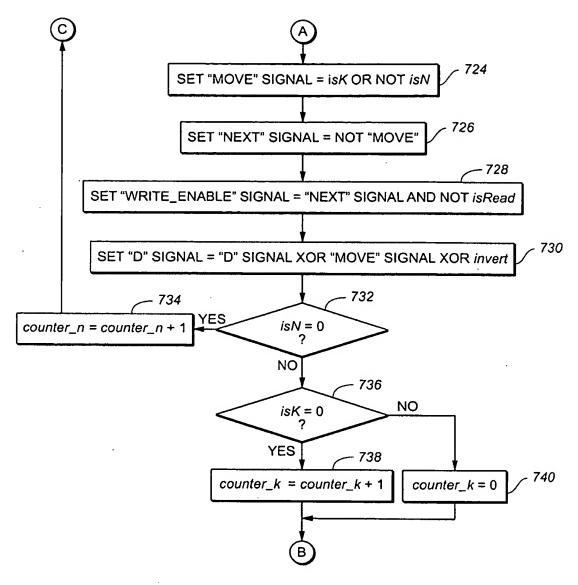


FIG._7B

.

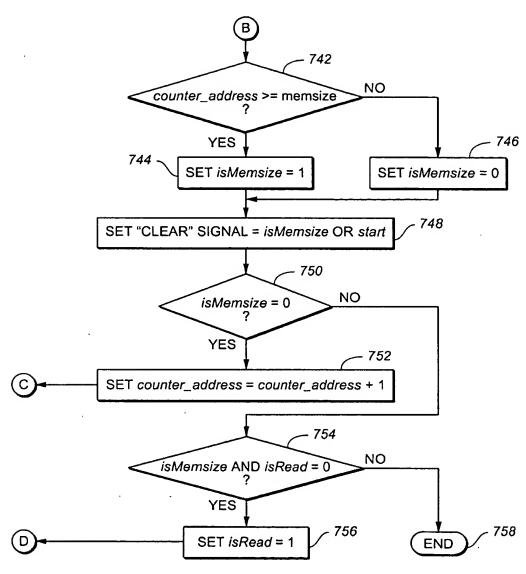


FIG._7C